Power MOSFET 75 Amps, 30 Volts N-Channel TO-220 and D²PAK

This Logic Level Vertical Power MOSFET is a general purpose part that provides the "best of design" available today in a low cost power package. Avalanche energy issues make this part an ideal design in. The drain-to-source diode has a ideal fast but soft recovery. **Features**

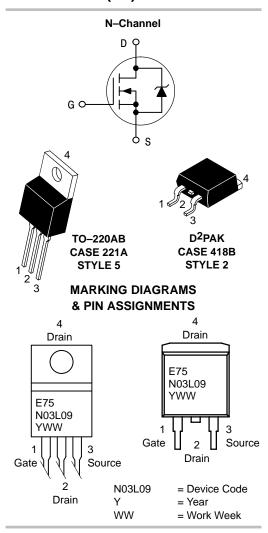
- Ultra-Low RDS(on), Single Base, Advanced Technology
- SPICE Parameters Available
- Diode is Characterized for Use in Bridge Circuits
- IDSS and VDS(on) Specified at Elevated Temperatures
- High Avalanche Energy Specified
- ESD JEDAC Rated HBM Class 1, MM Class B, CDM Class 0 Typical Applications
- Power Supplies
- Inductive Loads
- PWM Motor Controls
- Replaces MTP75N03HDL and MTB75N03HDL in Many Applications



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75 AMPERES 30 VOLTS RDS(on) = 9 mΩ



ORDERING INFORMATION

Device	Package	Shipping
NTP75N03L09	TO-220	50 Units/Rail
NTP75N03L09	D2PAK	50 Units/Rail
NTP75N03L09T4	D2PAK	800 Tape & Reel

MAXIMUM RATINGS (T_J = 25° C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V _{DSS}	30	Vdc
Drain-to-Gate Voltage (RGS = 10 M Ω)	VDGB	30	Vdc
Gate-to-Source Voltage - Continuous	V _{GS}	±20	Vdc
Non–repetitive (tp \leq 10 ms)	V _{GS}	±24	Vdc
Drain Current – Continuous @ $T_A = 25^{\circ}C$ – Continuous @ $T_A = 100^{\circ}C$ – Single Pulse (tp ≤ 10 µs)	I _D ID IDM	75 59 225	Adc Apk
Total Power Dissipation @ T _C = 25°C Derate above 25°C Total Power Dissipation @ T _A = 25°C (Note 1.)	PD	150 1.0 2.5	W W/°C W
Operating and Storage Temperature Range	T _J and T _{stg}	-55 to 150	°C
Single Pulse Drain–to–Source Avalanche Energy – Starting T _J = 25° C (V _{DD} = 38 Vdc, V _{GS} = 10 Vdc, L = 1 mH, I _L (pk) = 55 A, V _{DS} = 40 Vdc)	E _{AS}	1500	mJ
Thermal Resistance – Junction–to–Case – Junction–to–Ambient – Junction–to–Ambient (Note 1.)	R _{θJC} R _{θJA} R _{θJA}	1.0 62.5 50	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	тլ	260	°C

1. When surface mounted to an FR4 board using the minimum recommended pad size.

ELECTRICAL CHARACTERISTICS (T_A = 25° C unless otherwise noted)

Characteristic			Min	Тур.	Max	Unit
OFF CHARACTERISTICS						
Drain–Source Breakdown Voltage (Note 2.) ($V_{GS} = 0 \text{ Vdc}, I_D = 250 \mu \text{Adc}$) Temperature Coefficient (Negative)		V(BR)DSS	30	34 57		Vdc mV°C
Zero Gate Voltage Drain Current $(V_{DS} = 30 \text{ Vdc}, V_{GS} = 0 \text{ Vdc})$ $(V_{DS} = 30 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, T_J = 150^{\circ}\text{C})$		IDSS			1.0 10	μAdc
Gate-Body Leakage Current $(V_{GS} = \pm 20 \text{ Vdc}, V_{DS} = 0 \text{ Vdc})$			_	-	±100	nAdc
ON CHARACTERISTICS (Note 2.)					
Gate Threshold Voltage (Note 2.) $(V_{DS} = V_{GS}, I_D = 250 \mu Adc)$ Threshold Temperature Coefficient (Negative)			1.0 _	1.6 -6	2.0	Vdc mV°C
Static Drain–to–Source On–Resistance (Note 2.) $(V_{GS} = 5.0 \text{ Vdc}, I_D = 37.5 \text{ Adc})$		R _{DS(on)}	_	7.5	9	mΩ
Static Drain–to–Source On Resistance (Note 2.) ($V_{GS} = 10 \text{ Vdc}, I_D = 75 \text{ Adc}$) ($V_{GS} = 10 \text{ Vdc}, I_D = 37.5 \text{ Adc}, T_J = 125^{\circ}\text{C}$)			-	0.52 0.35	0.68 0.50	Vdc
Forward Transconductance (I	Notes 2. & 4.) $(V_{DS} = 3 \text{ Vdc}, I_D = 20 \text{ Adc})$	9FS	_	58	-	mΩ
DYNAMIC CHARACTERIS	STICS (Note 4.)					
Input Capacitance		C _{iss}	-	4398	5635	pF
Output Capacitance	(V _{DS} = 25 Vdc, V _{GS} = 0, f = 1.0 MHz)	C _{OSS}	_	1160	1894	
Transfer Capacitance		C _{rss}	_	317	430	
SWITCHING CHARACTER	RISTICS (Notes 3. & 4.)					
Turn–On Delay Time		^t d(on)	_	31	48	ns
Rise Time	(V _{GS} = 5.0 Vdc, V _{DD} = 20 Vdc, I _D = 75 Adc,	t _r	_	510	986	-
Turn-Off Delay Time	$R_{G} = 4.7 \Omega$ (Note 2.)	^t d(off)	_	99	120	
Fall Time		t _f	_	203	300	
Gate Charge	(V _{GS} = 5.0 Vdc, I _D = 75 Adc, V _{DS} = 24 Vdc) (Note 2.)	QT	-	52	122	nC
		Q ₁	_	6.6	28	-
		Q ₂	_	28	66	
SOURCE-DRAIN DIODE	CHARACTERISTICS					
Forward On–Voltage	$(I_{S} = 75 \text{ Adc}, V_{GS} = 0 \text{ Vdc})$ $(I_{S} = 75 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, T_{J} = 125^{\circ}\text{C})$ (Note 2.)	V _{SD}	_	1.19 1.09	1.25 -	Vdc
Reverse Recovery Time		t _{rr}	-	37	-	ns
(Note 4.)	(I _S = 75 Adc, V _{GS} = 0 Vdc dl _S /dt = 100 A/μs) (Note 2.)	ta	-	20	-	
Devene Developme Oterred		4.		47		

Pulse Test: Pulse Width ≤ 300 µS, Duty Cycle ≤ 2%.
Switching characteristics are independent of operating junction temperatures.

4. From characterization test data.

Reverse Recovery Stored Charge (Note 4.)

tb Q_{RR} _

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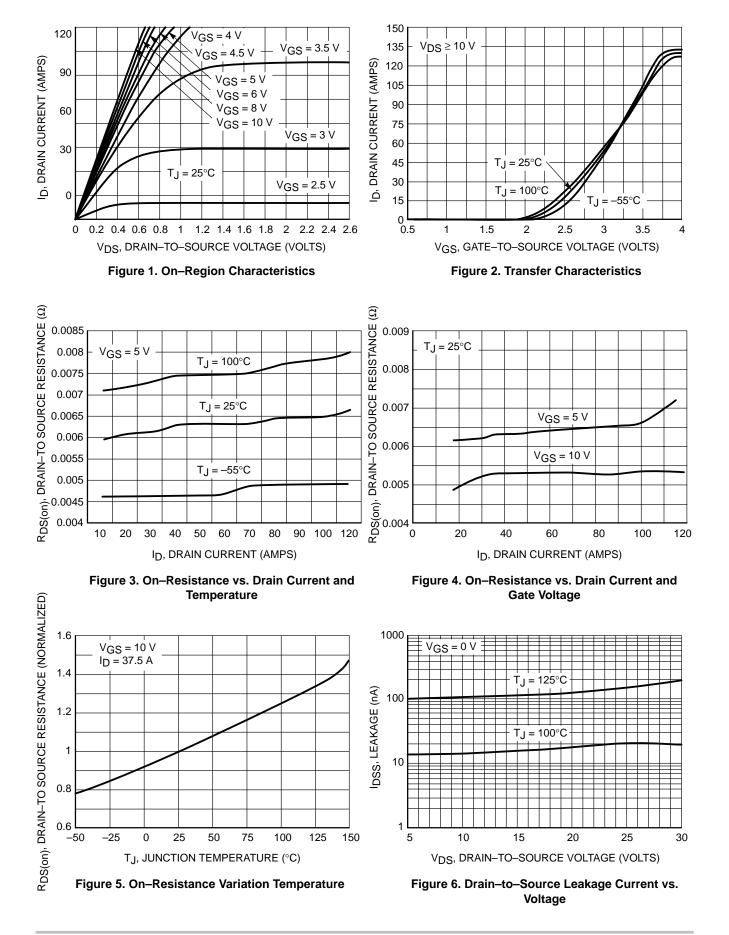
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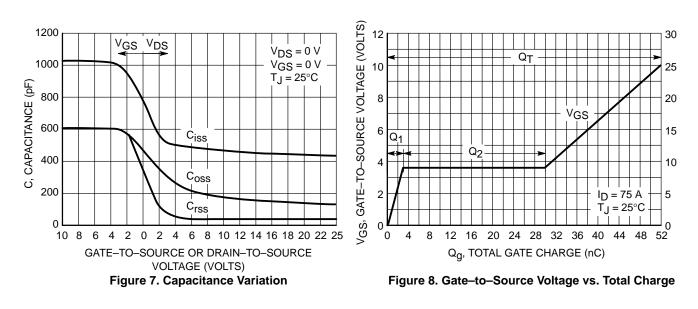
0.023

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μC





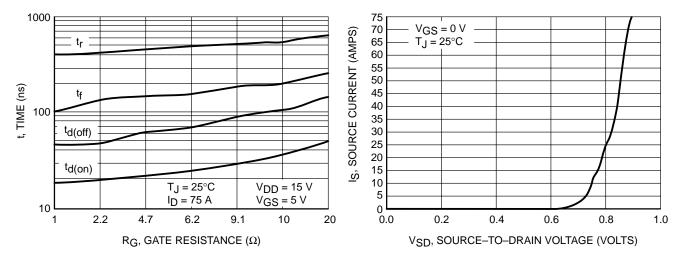
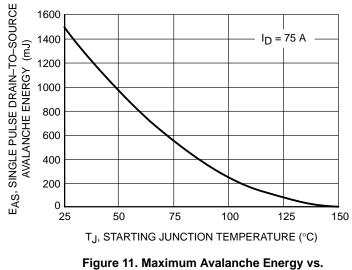


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

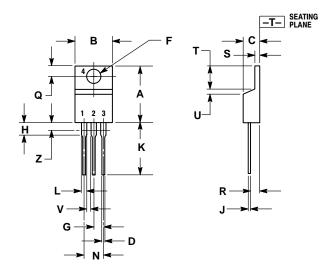
Figure 10. Diode Forward Voltage vs. Current



Starting Junction Temperature

PACKAGE DIMENSIONS

TO-220 THREE-LEAD TO-220AB CASE 221A-09 **ISSUE AA**

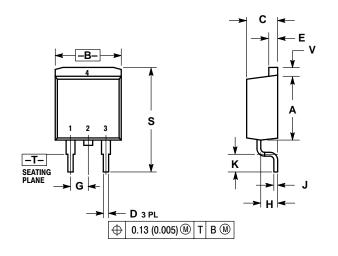


NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

DIM MIN MAX MIN M A 0.570 0.620 14.48 15 B 0.380 0.405 9.66 10 C 0.160 0.190 4.07 4 D 0.025 0.035 0.64 0 G 0.095 0.105 2.42 2 H 0.110 0.155 2.80 2 J 0.018 0.025 0.46 0 K 0.560 1.55 2.80 2 J 0.018 0.025 0.46 0 K 0.500 0.562 12.70 14 L 0.495 0.080 1.15 1 N 0.190 0.210 4.83 5 Q 0.100 0.120 2.54 2 R 0.080 0.110 2.04 2 S 0.045 0.055 1.15 1
B 0.380 0.405 9.66 10 C 0.160 0.190 4.07 4 D 0.025 0.035 0.64 0 F 0.142 0.147 3.61 3 G 0.095 0.105 2.42 2 H 0.110 0.155 2.80 3 J 0.018 0.025 0.46 0 K 0.500 0.562 12.70 14 L 0.045 0.060 1.15 1 N 0.190 0.210 4.83 5 Q 0.100 0.122 2.54 3 R 0.080 0.110 2.042 3
C 0.160 0.190 4.07 4 D 0.025 0.035 0.64 0 F 0.142 0.147 3.61 3 G 0.095 0.105 2.42 2 H 0.110 0.155 2.80 3 J 0.018 0.025 0.46 0 K 0.500 0.562 12.70 14 L 0.045 0.060 1.15 1 N 0.190 0.210 4.83 5 Q 0.100 0.120 2.54 3
D 0.025 0.035 0.64 0 F 0.142 0.147 3.61 3 G 0.095 0.105 2.42 2 H 0.110 0.155 2.80 3 J 0.018 0.025 0.46 0 K 0.500 0.562 12.70 14 L 0.045 0.060 1.15 1 N 0.190 0.210 4.83 5 Q 0.100 0.220 2.54 3 R 0.080 0.110 2.044 2
F 0.142 0.147 3.61 3 G 0.095 0.105 2.42 2 H 0.110 0.155 2.80 3 J 0.018 0.025 0.46 0 K 0.500 0.562 12.70 14 L 0.045 0.060 1.15 1 N 0.190 0.210 4.83 5 Q 0.100 0.120 2.54 3 R 0.080 0.110 2.044 3
G 0.095 0.105 2.42 2 H 0.110 0.155 2.80 3 J 0.018 0.025 0.46 0 K 0.500 0.562 12.70 14 L 0.045 0.060 1.15 1 N 0.190 0.210 4.83 5 Q 0.100 0.120 2.54 3 R 0.080 0.110 2.042 3
H 0.110 0.155 2.80 3 J 0.018 0.025 0.46 0 K 0.500 0.562 12.70 14 L 0.045 0.060 1.15 1 N 0.190 0.210 4.83 5 Q 0.100 0.120 2.54 3 R 0.080 0.110 2.044 3
J 0.018 0.025 0.46 0 K 0.500 0.562 12.70 14 L 0.045 0.060 1.15 1 N 0.190 0.210 4.83 5 Q 0.100 0.120 2.54 5 R 0.080 0.110 2.04 2
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Q 0.100 0.120 2.54 3 R 0.080 0.110 2.04 2
R 0.080 0.110 2.04 2
S 0.045 0.055 1.15 1
T 0.235 0.255 5.97 6
U 0.000 0.050 0.00 1
V 0.045 1.15
Z 0.080 2

PACKAGE DIMENSIONS

D²PAK CASE 418B-03 ISSUE D



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.340	0.380	8.64	9.65	
в	0.380	0.405	9.65	10.29	
С	0.160	0.190	4.06	4.83	
D	0.020	0.035	0.51	0.89	
Е	0.045	0.055	1.14	1.40	
G	0.100 BSC		2.54 BSC		
Η	0.080	0.110	2.03	2.79	
L	0.018	0.025	0.46	0.64	
Κ	0.090	0.110	2.29	2.79	
S	0.575	0.625	14.60	15.88	
٧	0.045	0.055	1.14	1.40	

STYLE 2: PIN 1. GATE 2. DRAIN 3. SOURCE 4. DRAIN

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